

X-1435 US
10/714,380

PATENT
Conf. No.: 1825

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger
Assignee: Xilinx, Inc.
Title: System and Method for Securing Using Decryption Keys During
FPGA Configuration Using a Microcontroller
Serial No.: 10/714,380 Filed: 10/31/2003
Examiner: Randal D. Moran Art Unit: 2435
Docket No.: X-1435 US Conf. No.: 1825

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

Dear Sir:

In response to the Non-Final Office Action dated January 23, 2009, please
amend the above-referenced application as indicated.

Amendments to the Claims are reflected in the listing of claims which begins
on page 2 of this paper.

Remarks begin on page 7 of this paper.